

U.T. ADMINISTRATION OF DADRA & NAGAR HAVELI  
AND DAMAN & DIU  
OFFICE OF THE PRINCIPAL  
GOVERNMENT POLYTECHNIC DIU,  
EDUCATION HUB, KEVDI- DIU (U.T.)

No. 3.1/ESTT-GP/DIU/RECRUIT/24-25/198

Date: 03/01/2025

**ADVERTISEMENT FOR WALK-IN-INTERVIEW**

Government Polytechnic, Diu invites applications from eligible candidates for empanelment of visiting faculty post for Lecturer in Electrical Engineering. Due date for submission of application is **10/01/2025**, at 12:00 hrs.

**Education Qualification for Diploma Faculty**

Sr. No.	Name of Subject	No. of Posts	Education Qualifications
1	Lecturer in Electrical Engineering	Visiting : 01	<b>Required Qualification:</b> B.E / B.Tech from recognized University with First Class.

**HONORARIUM:** - Rs. 250/- per Lecture. (Max. upto 20,000/- per month)

**Date of Interview:** 15/01/2025

**Venue of Interview:** Collectorate, Diu at 10.00 am

**GENERAL CONDITIONS:**

1. The candidates should email their applications in prescribed format along with self-attested scanned copies of requisite documents (SSC & HSC marksheet, Degree marksheets + Certificate, Masters marksheets + Certificate, Experience letter if any) and Identity proof (Adhaar card) on or before 10/01/2025 upto 12:00 P.M (Email id: polytechnicdiu@gmail.com)
2. Incomplete applications/applications without necessary documents will summarily be rejected.
3. Original Documents should be produced at the time of interview for verification.
4. Application received after due date & time will be considered as rejected.
5. Any addendum/dedendum/corrigendum shall be posted on respective mail-ids only.
6. Only shortlisted & eligible candidates will be contacted for interview round.
7. The above posts are purely on visiting basis and the department has right to terminate the post anytime.
8. The selected candidate will not have any rights to claim for permanent post in future.
9. The selected candidate will be called only as per requirements of the institute.

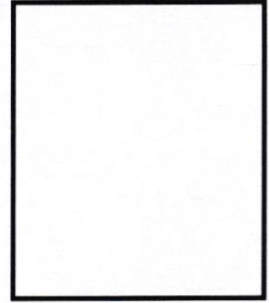
  
Principal,

Government Polytechnic Diu

**GOVERNMENT POLYTECHNIC, DIU**

**UT Administration of DNH & DD, Diu**

**APPLICATION FORM FOR LECTURER ON VISITING BASIS  
IN ELECTRICAL ENGINEERING**



1. Full Name: \_\_\_\_\_
2. Father's/Spouse's Name: \_\_\_\_\_
3. Date of Birth: \_\_\_/\_\_\_/\_\_\_
4. Gender: \_\_\_\_\_
5. Address: \_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_
6. Contact No.: \_\_\_\_\_/ \_\_\_\_\_
7. Email: \_\_\_\_\_
8. Nationality: \_\_\_\_\_
9. Marital Status: \_\_\_\_\_
10. Category SC/ST/OBC/PH/OTHERS: \_\_\_\_\_
11. If selected, how soon you can join the organization: \_\_\_\_\_ Days
12. Education Qualifications:

Sr. No	Examination/ Degree	Subject(s)	University/ Board	Percentage/ Grade	Year
1.	SSC				
2.	HSC				
3.	Diploma				
4.	Degree B.E/ B.Tech				

5.	Masters M.E/M.Tech				
6.	Ph.D				
7.					
8.					
9.					

12. Details of Experience: (Total Teaching Experience: \_\_\_\_\_ Years \_\_\_\_\_ Months)

Sr.No.	Institution/Employer	Designation	From	To

13. Publications:

Sr.No.	Books	Articles/Papers

I hereby declare that information given above is correct and no relevant information has been concealed.

Place:

Date:

(Signature of Applicant)

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